



FIG. 1

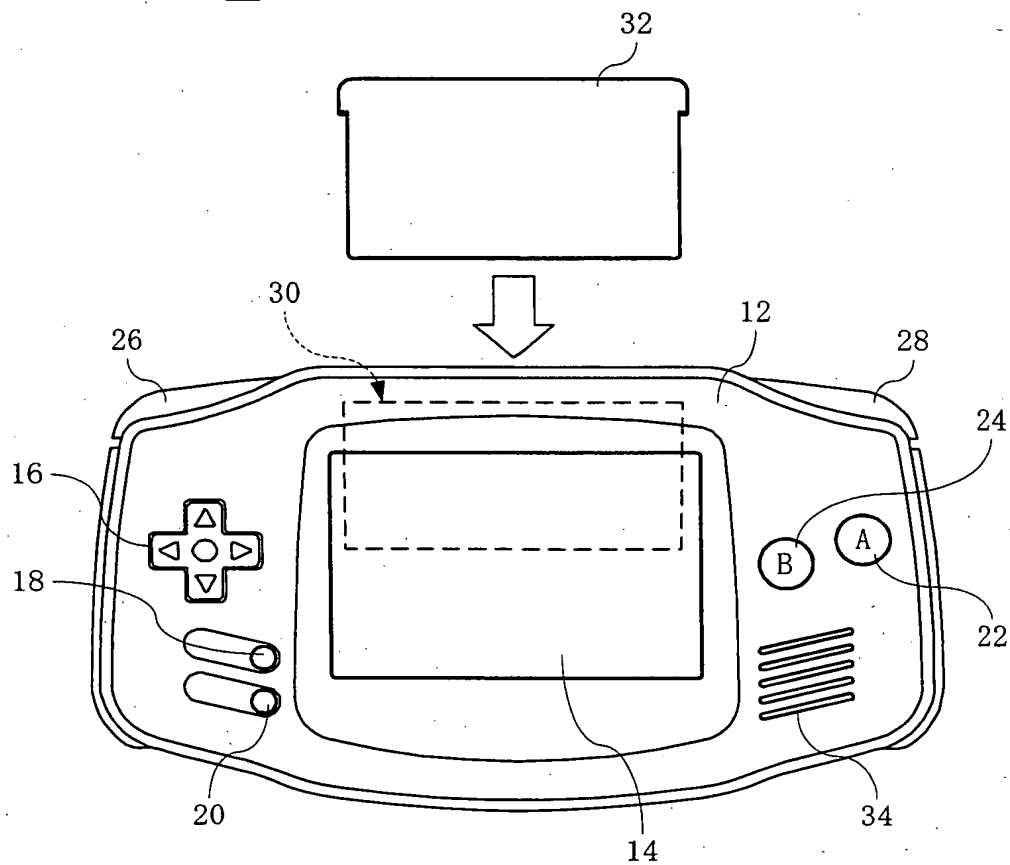


FIG. 2

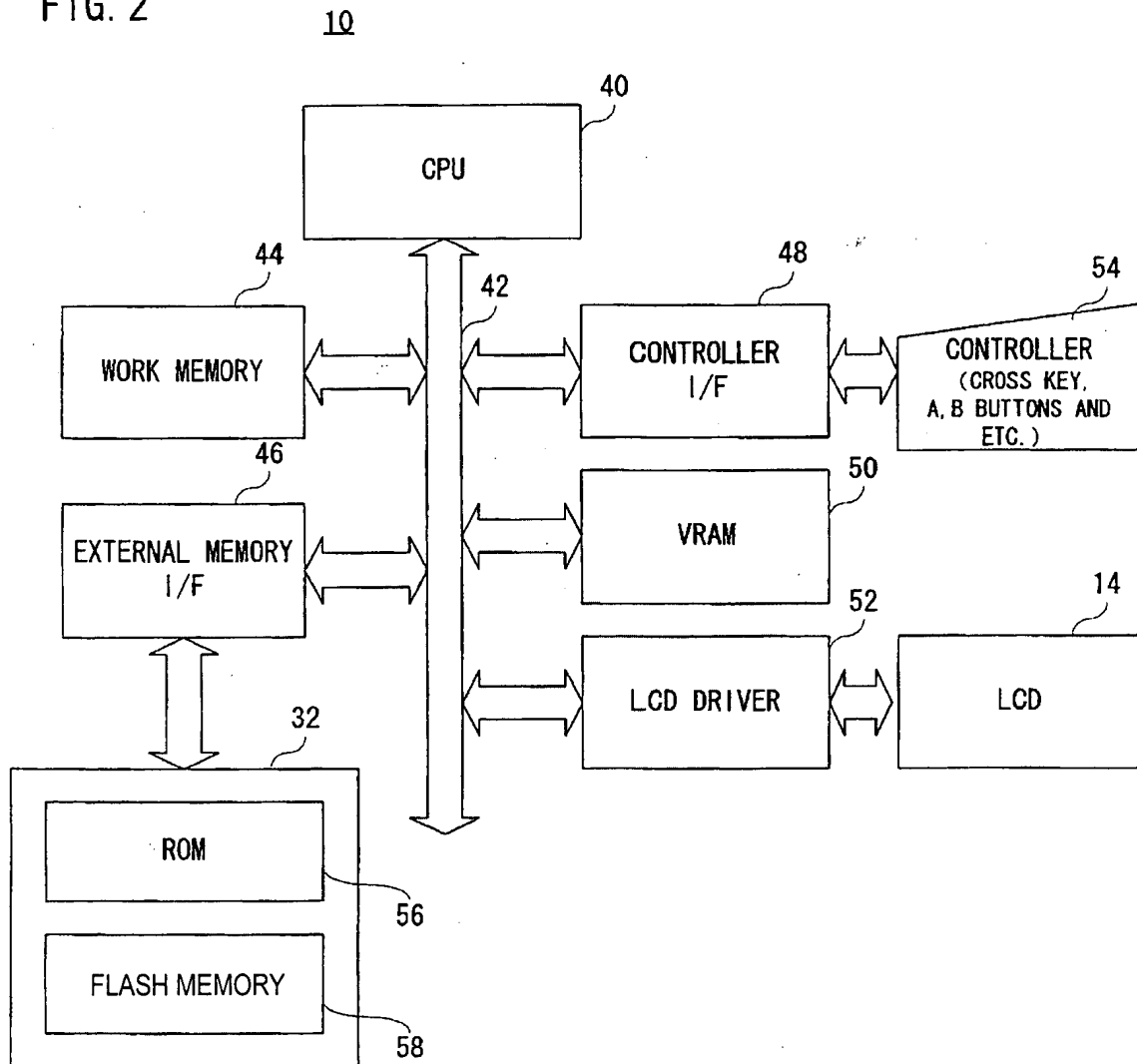


FIG. 3

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MEMORY MAP OF ROM

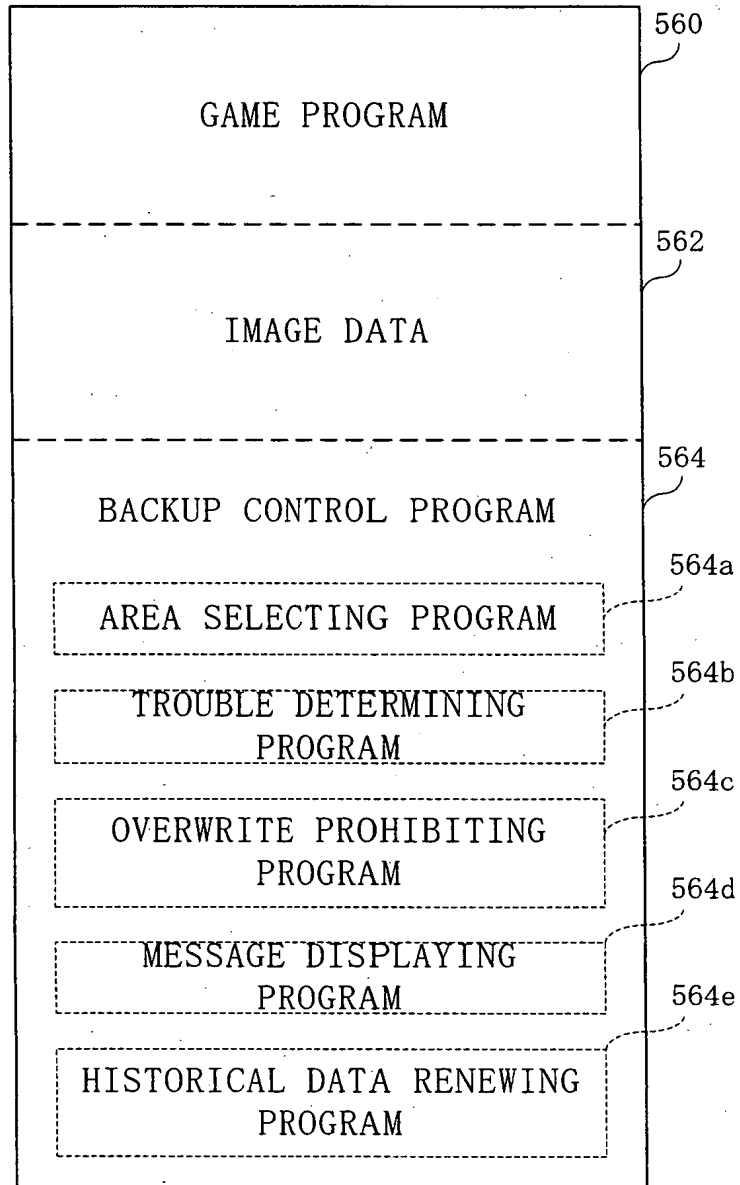
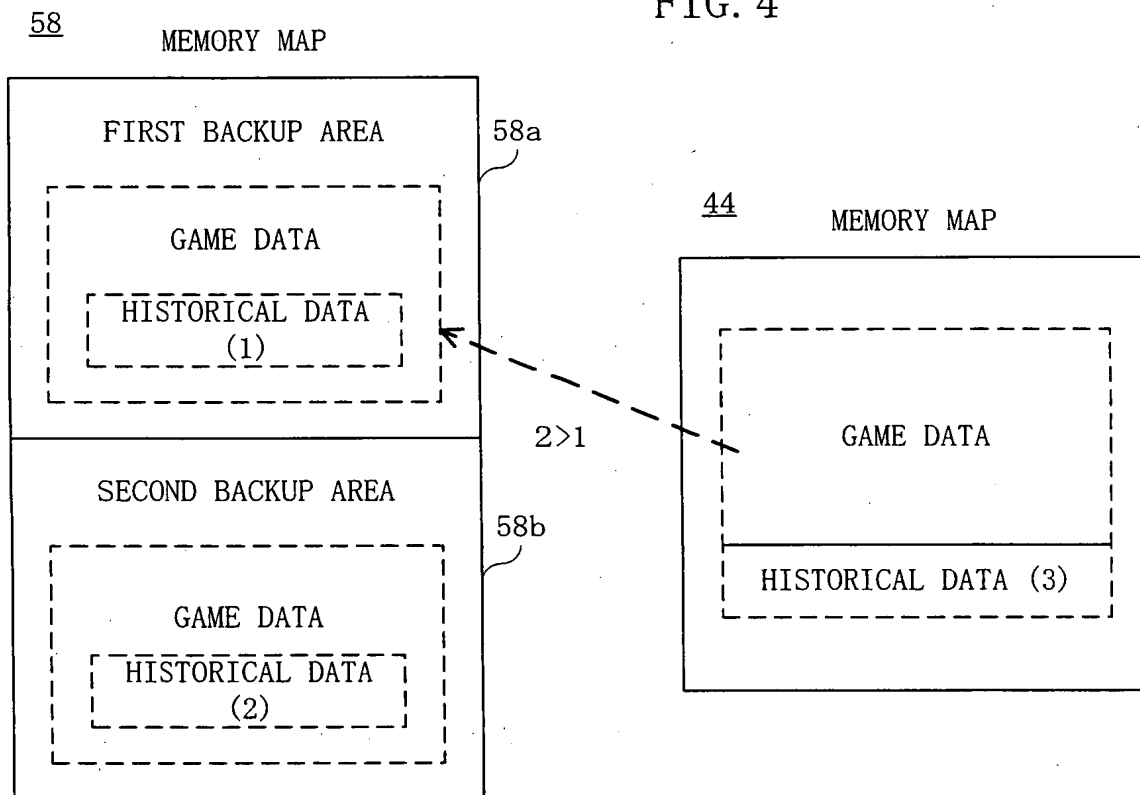


FIG. 4

(A)



(B)

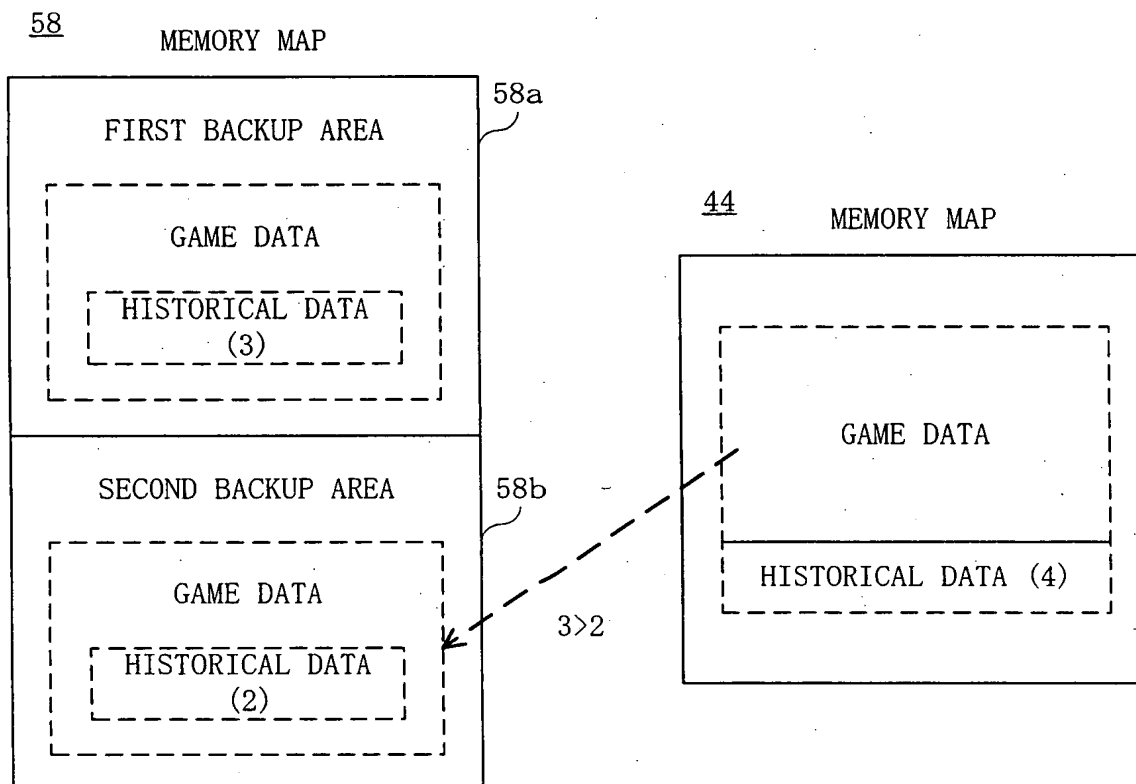
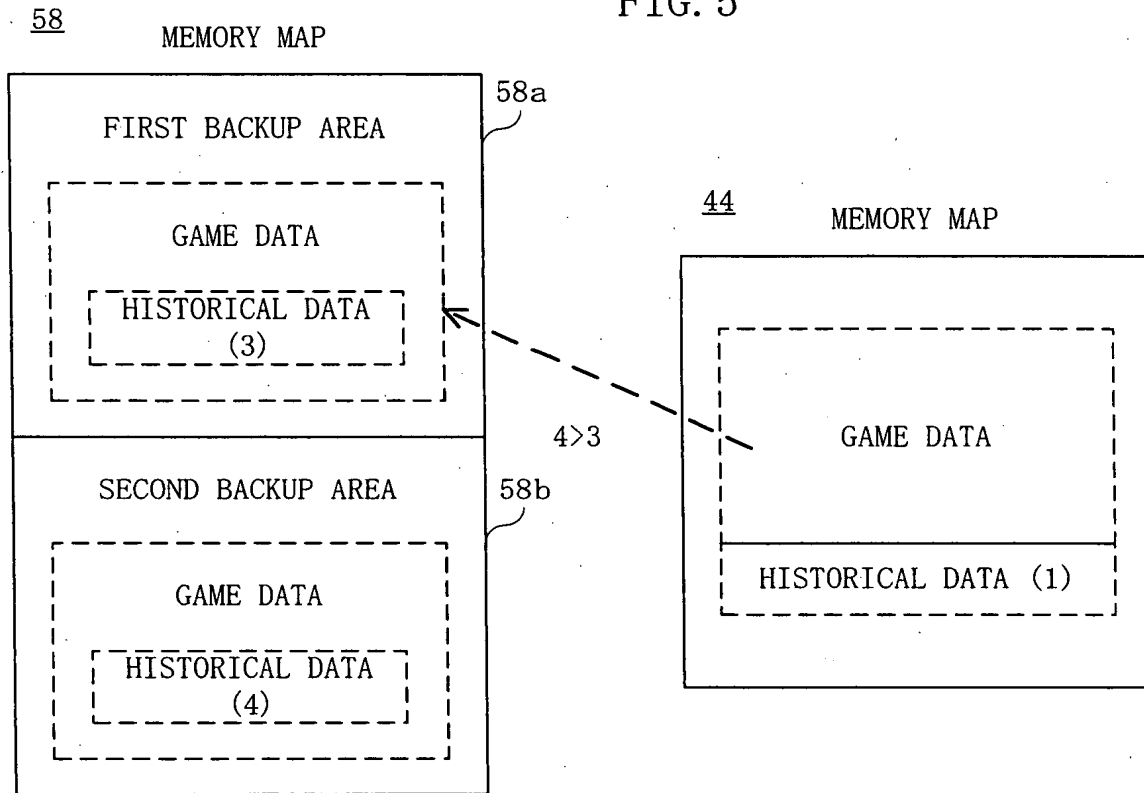


FIG. 5

(A)



(B)

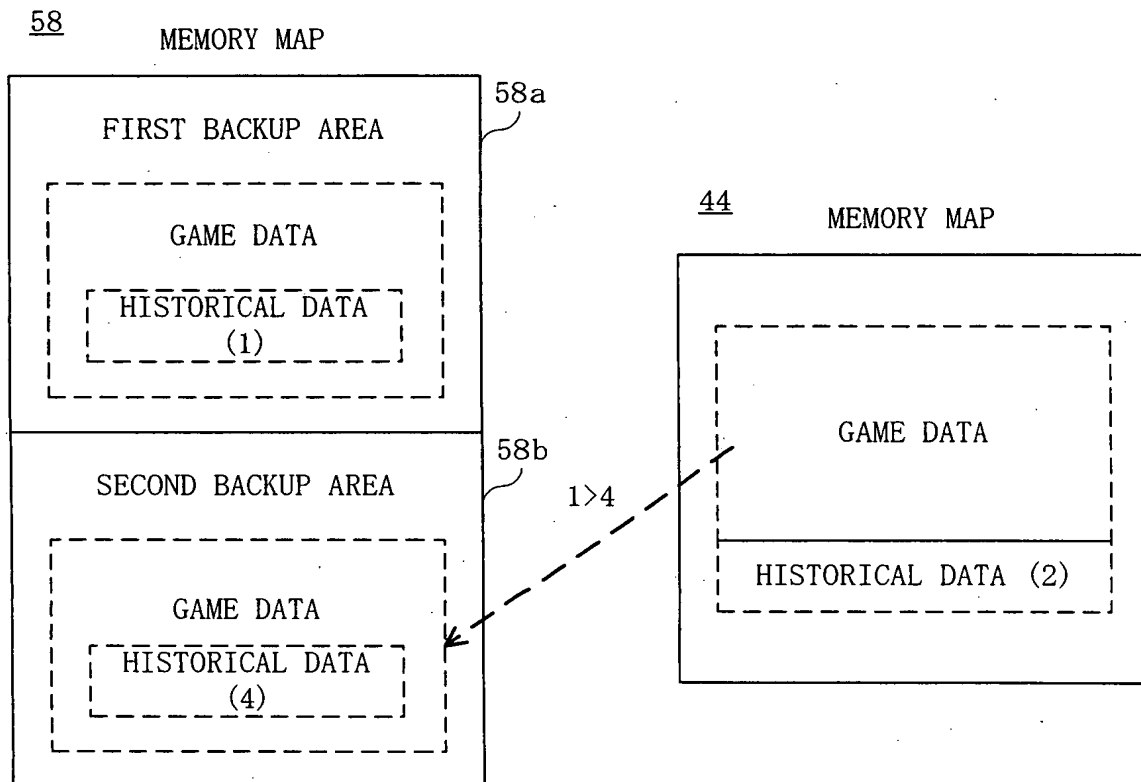


FIG. 6

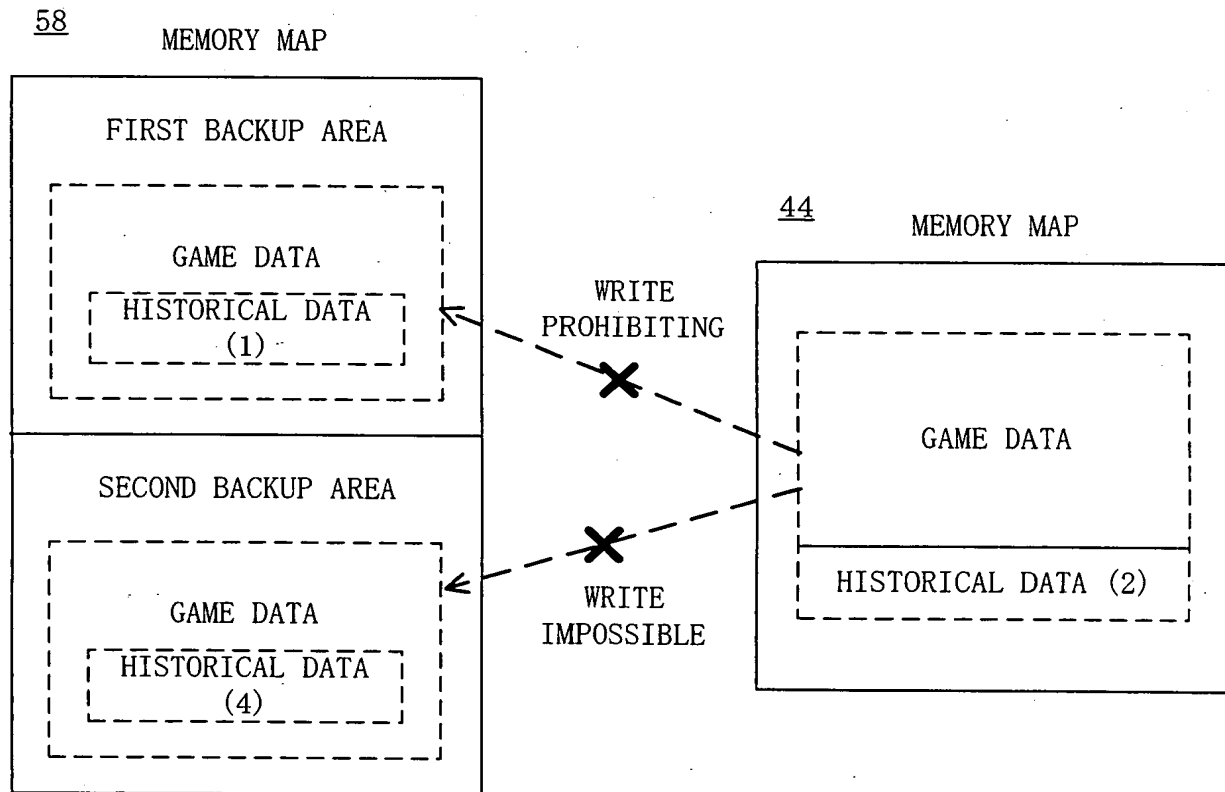
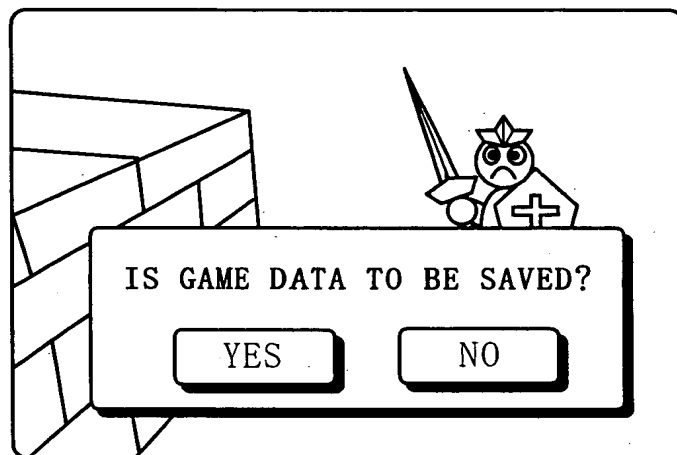
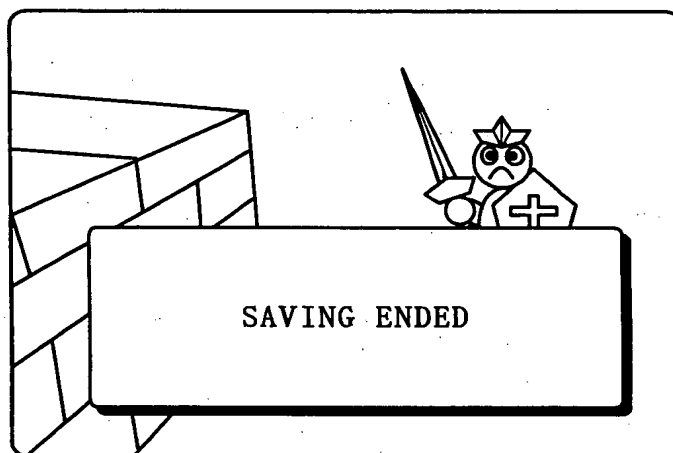


FIG. 7

(A)



(B)



(C)

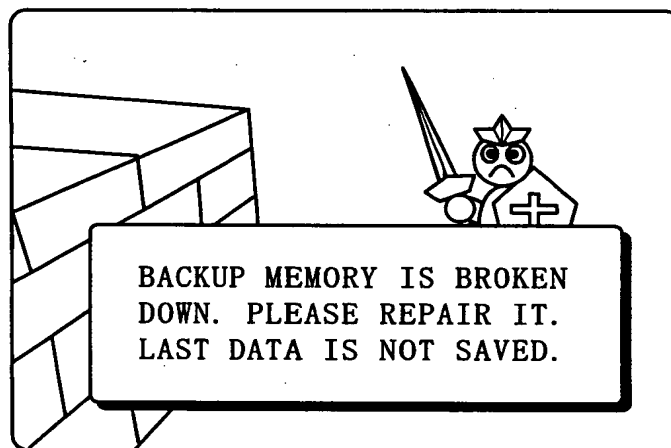


FIG. 8

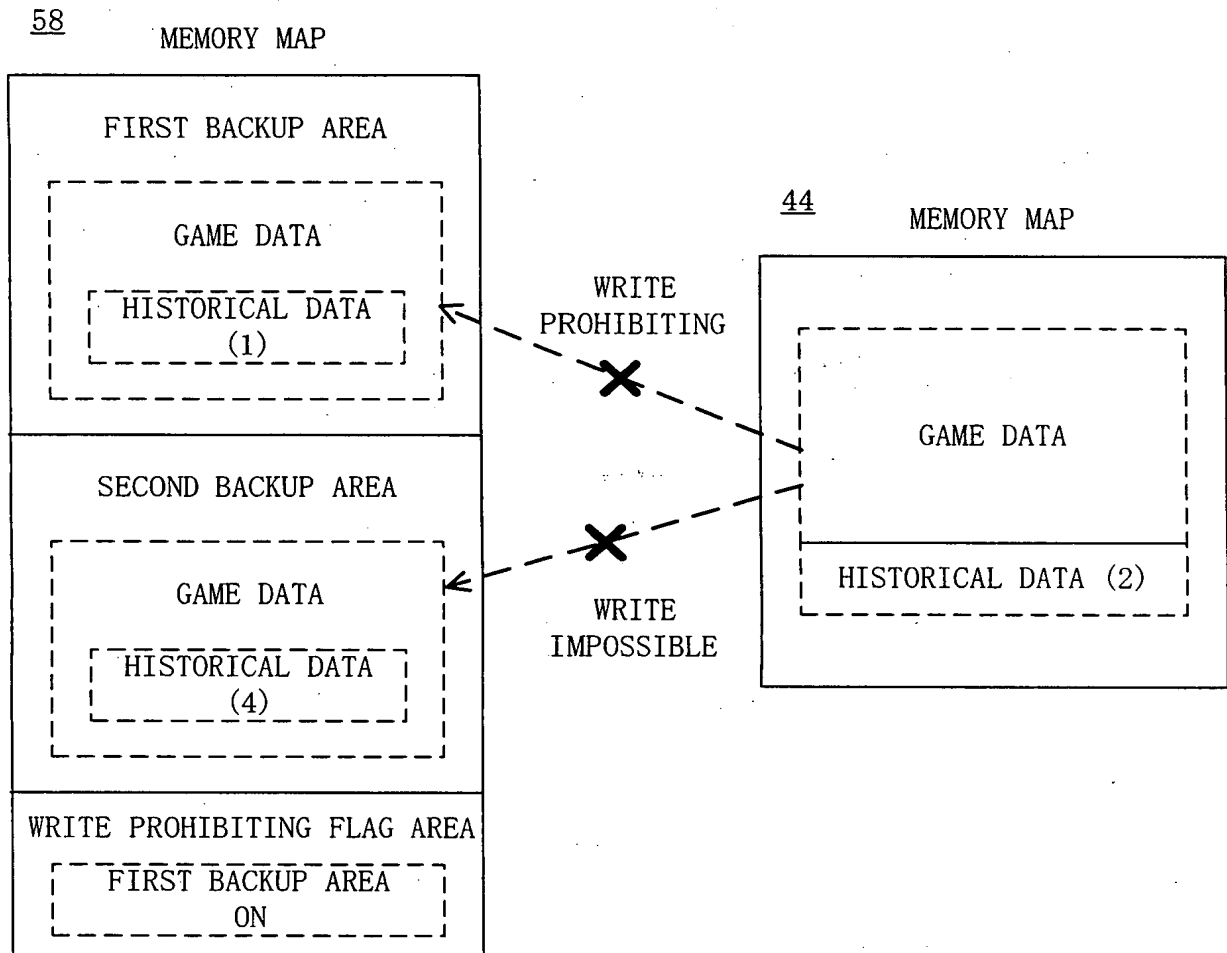




FIG. 9

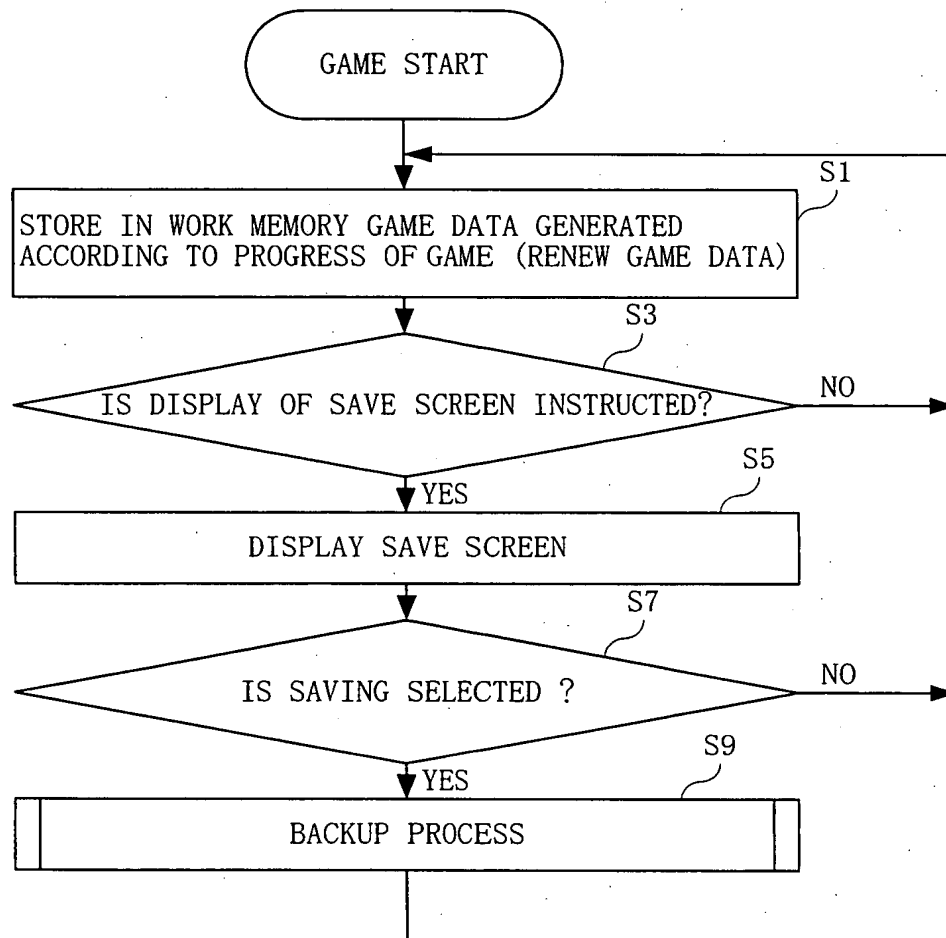


FIG. 10

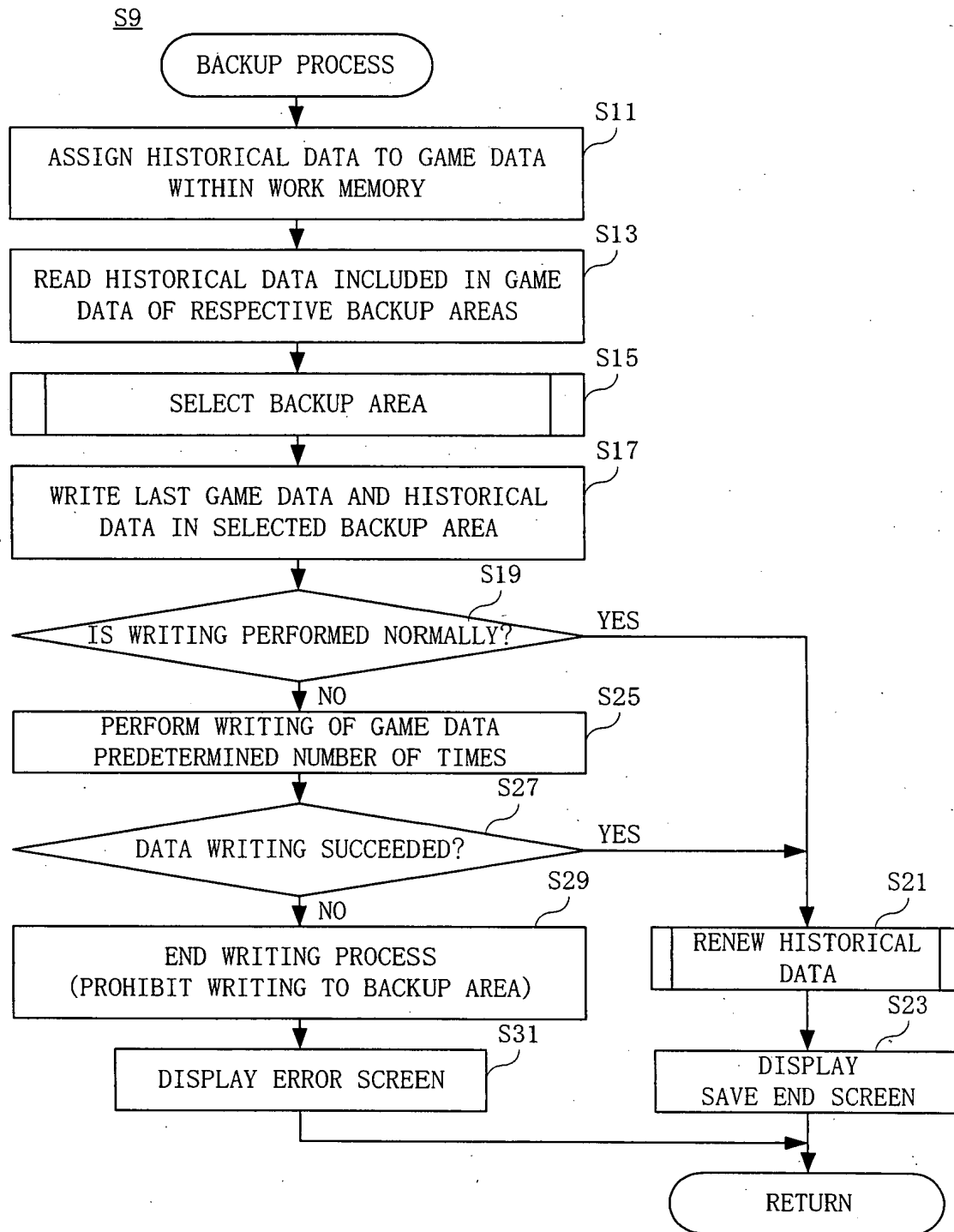


FIG. 11

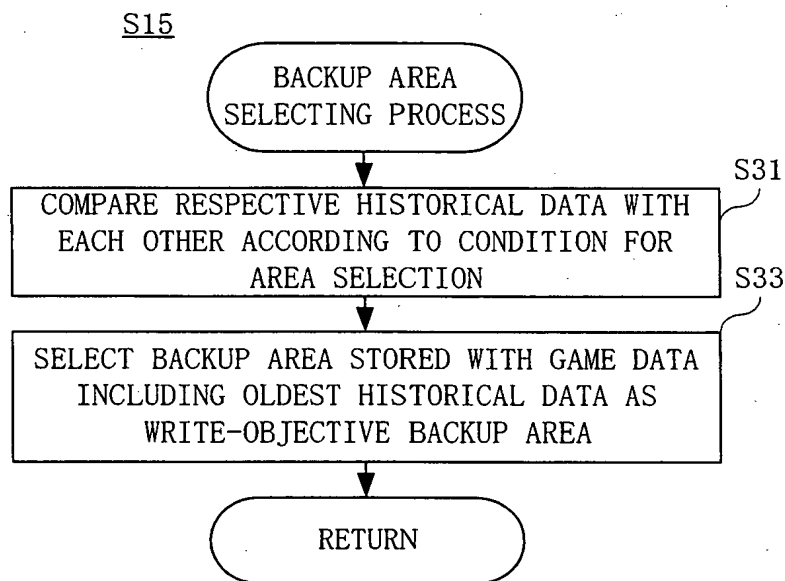


FIG. 12

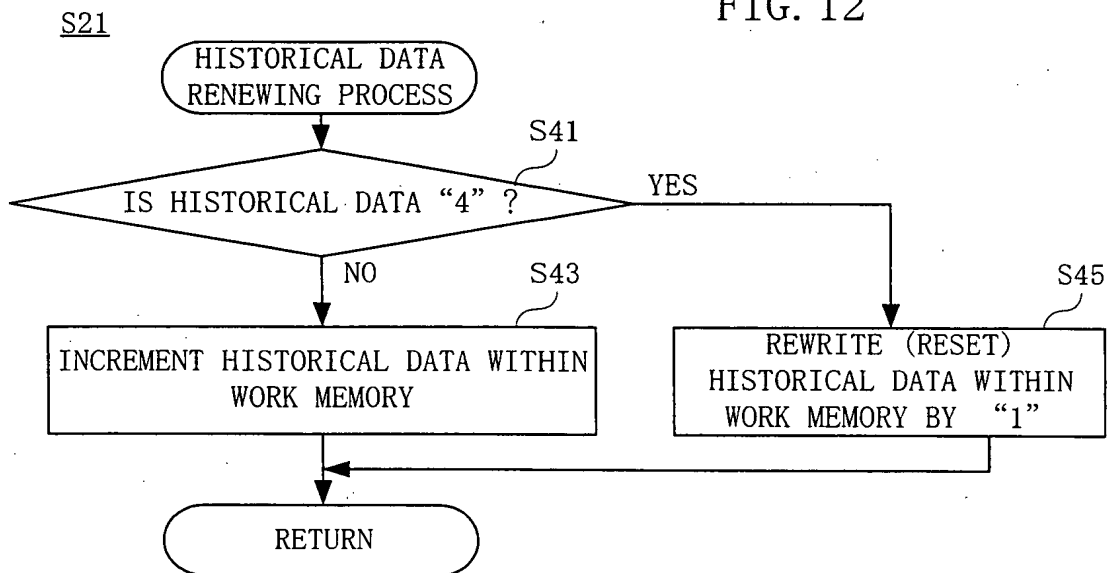


FIG. 13

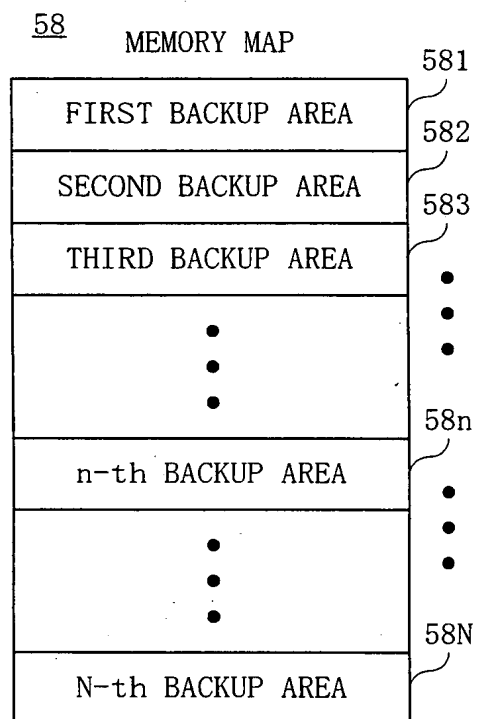


FIG. 14

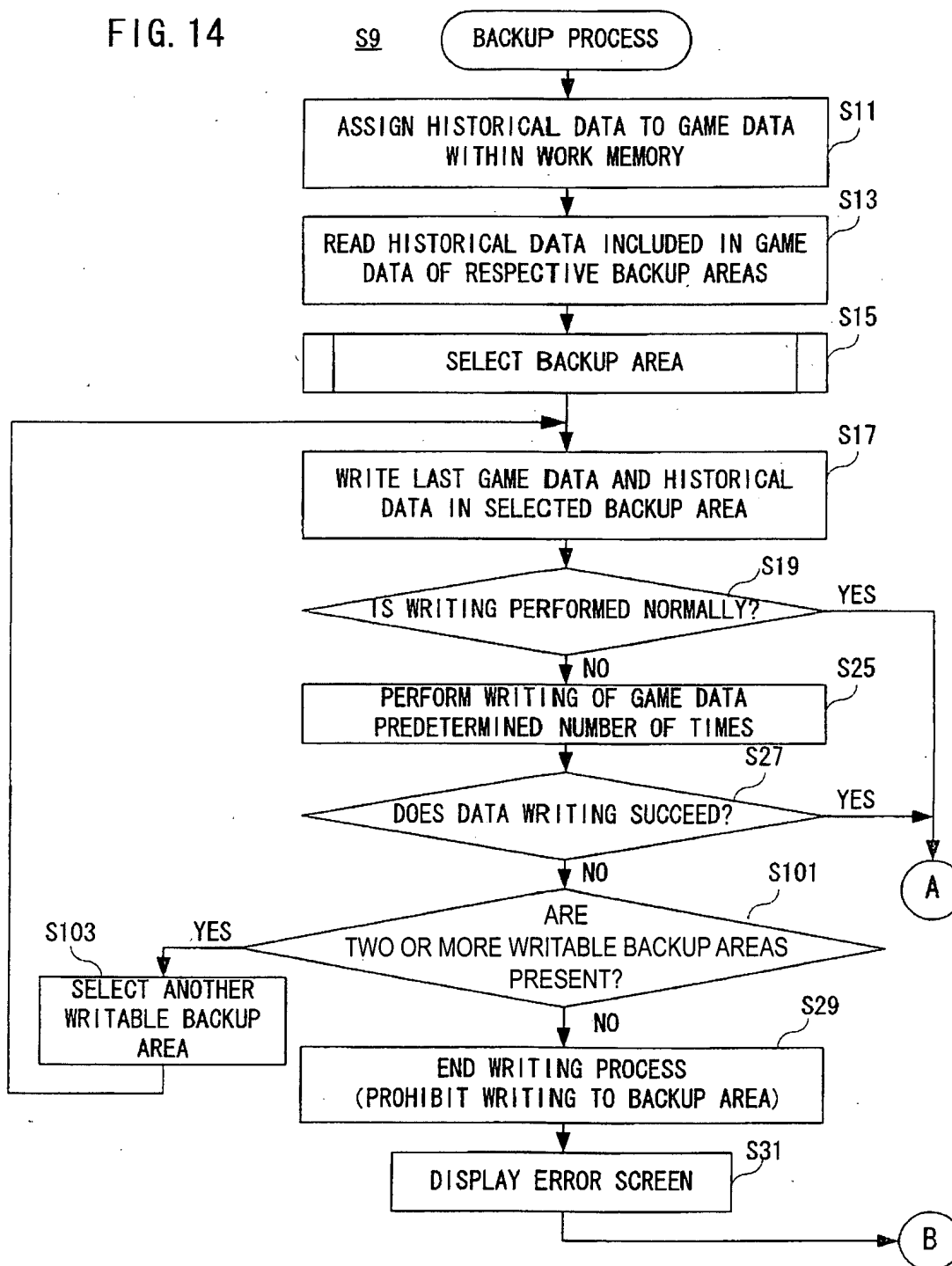


FIG. 15

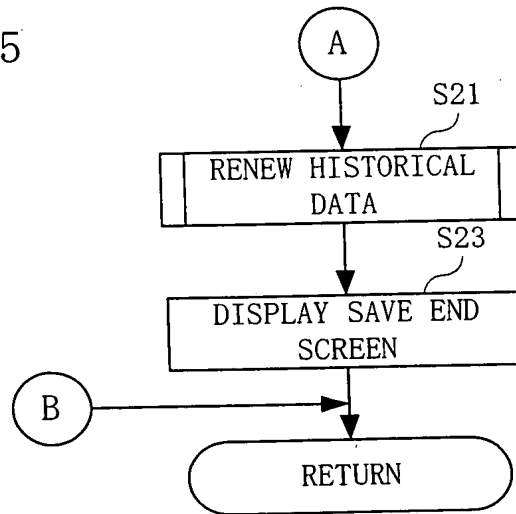


FIG. 16

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MEMORY MAP

